

FIG. 5



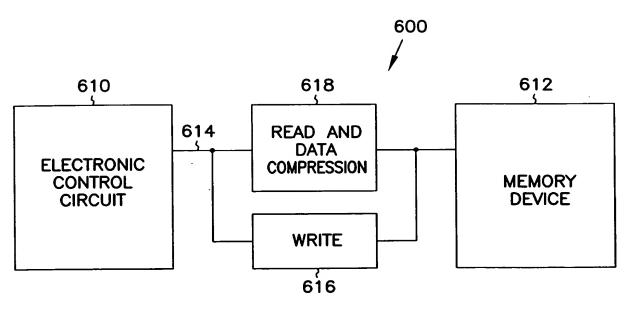


FIG. 6

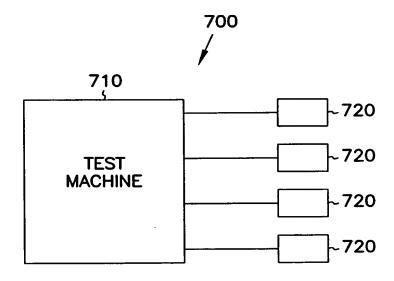
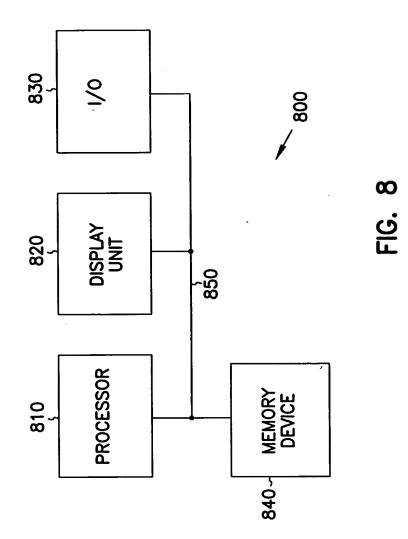


FIG. 7

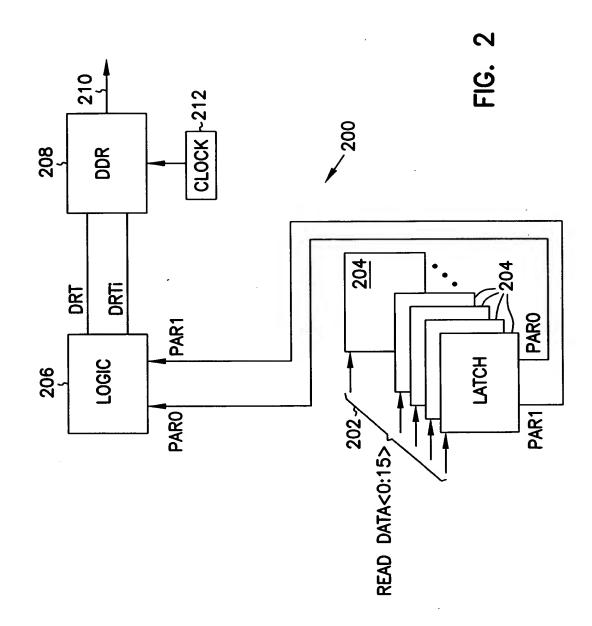


COCTOR BORDE

ARRAY OF MEMORY CELLS 102 MEMORY DEVICE ADDRESSING CIRCUIT DATA LINES ADDRESS LINES CONTROL LINES 120 122 ELECTRONIC CONTROL CIRCUIT

FIG. 1

APPROVED	O.G. FIG.	
BY	CLASS	รบอดมเละ
DRAFTSMAN		



OPECE_OISOSO

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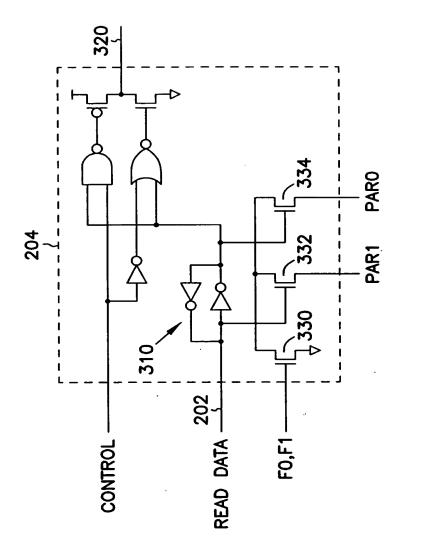


FIG. 3

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APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DULFTSMAN		

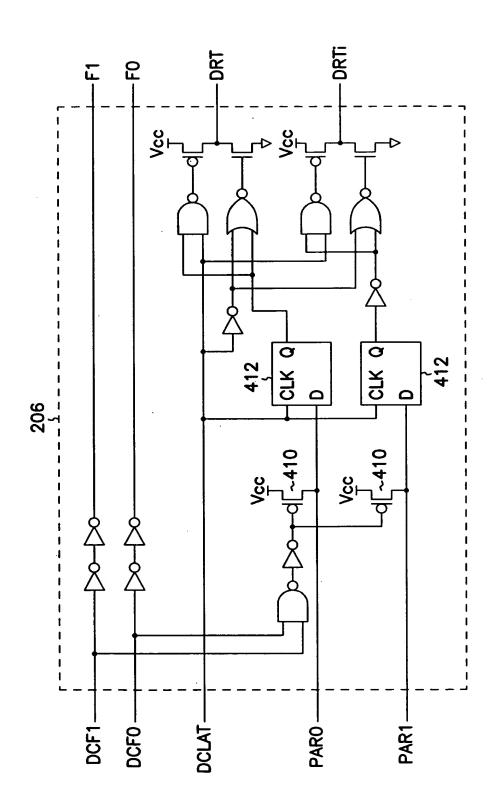


FIG. 4